

<IGBT Modules>

CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE
INSULATED TYPE

RX



Collector current I_C **1 0 0 A**
Collector-emitter voltage V_{CES} **1 2 0 0 V**
Maximum junction temperature T_{vjmax} **1 7 5 °C**

- Flat base type
- Copper base plate (Nickel-plating)
- RoHS Directive compliant
- Tin-plating pin terminals

RXP



Collector current I_C **1 0 0 A**
Collector-emitter voltage V_{CES} **1 2 0 0 V**
Maximum junction temperature T_{vjmax} **1 7 5 °C**

- Flat base type
- Copper base plate (Nickel-plating)
- RoHS Directive compliant
- Tin-plating pressfit terminals

sevenpack (three-phase bridge+Brake chopper)

- UL Recognized under UL1557, File No. E323585

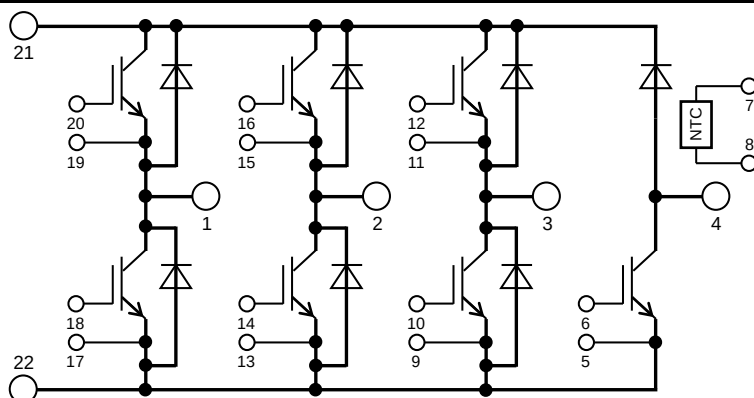
APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

OPTION (Below options are available.)

- PC-TIM (Phase Change Thermal Interface Material) pre-apply (Note10)

INTERNAL CONNECTION



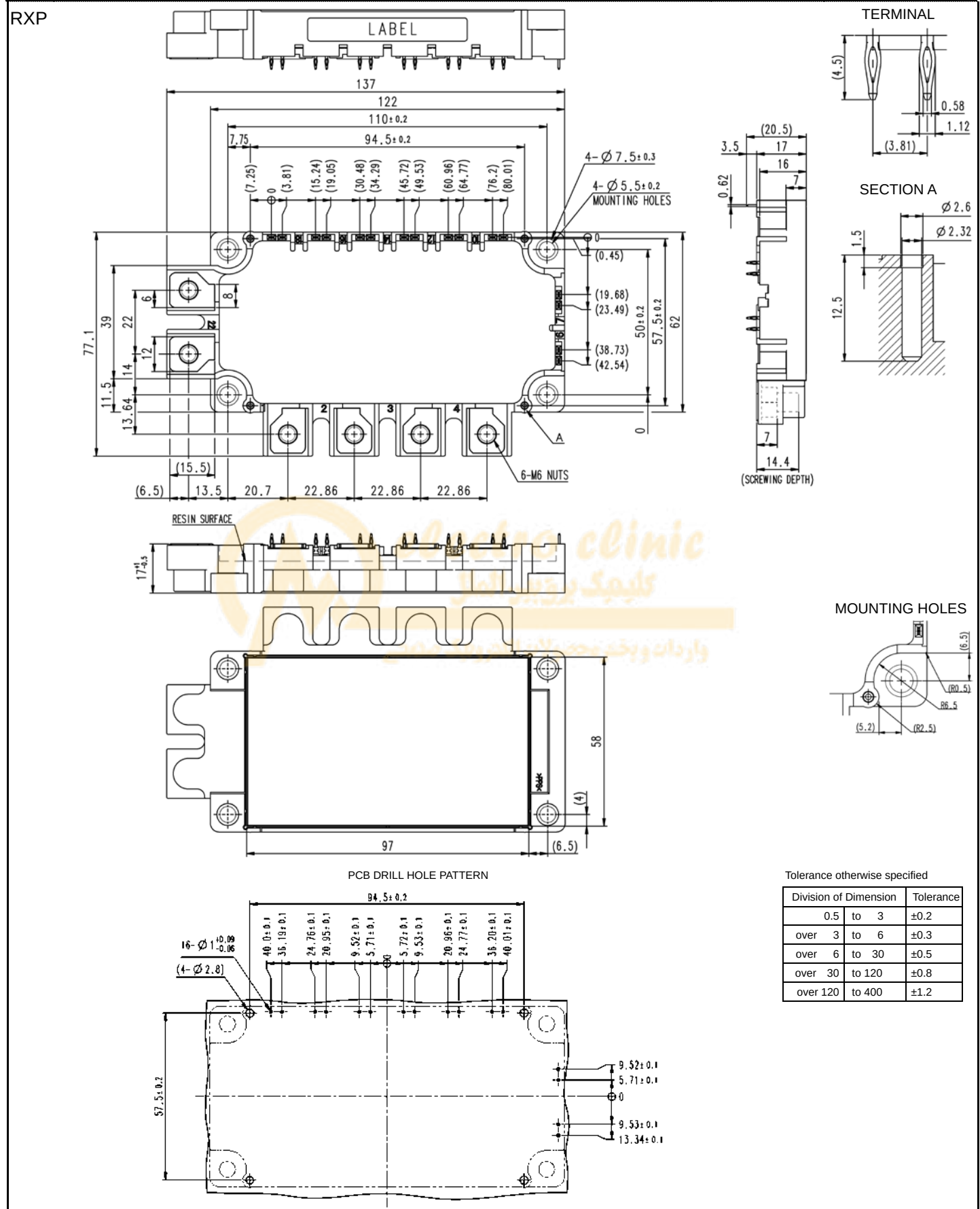
Terminal code

1 U	13 EVN
2 V	14 GVN
3 W	15 EVP
4 B	16 GVP
5 EB	17 EUN
6 GB	18 GUN
7 TH1	19 EUP
8 TH2	20 GUP
9 EWN	21 P
10 GWN	22 N
11 EWP	23 EVN
12 GWP	

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OUTLINE DRAWING

CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE
INSULATED TYPEMAXIMUM RATINGS ($T_{vj}=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, $T_C=119\text{ }^{\circ}\text{C}$ (Note2, 4)	100	A
I_{CRM}		Pulse, Repetitive (Note3)	200	
P_{tot}	Total power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note2, 4)	565	W
I_E (Note1)	Emitter current	DC (Note2)	100	A
I_{ERM} (Note1)		Pulse, Repetitive (Note3)	200	

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, $T_C=121\text{ }^{\circ}\text{C}$ (Note2, 4)	75	A
I_{CRM}		Pulse, Repetitive (Note3)	150	
P_{tot}	Total power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note2, 4)	440	W
V_{RRM}	Repetitive peak reverse voltage	G-E short-circuited	1200	V
I_F	Forward current	DC (Note2)	75	A
I_{FRM}		Pulse, Repetitive (Note3)	150	

MODULE

Symbol	Item	Conditions	Rating	Unit
V_{isol}	Isolation voltage	Terminals to base plate, RMS, $f=60\text{ Hz}$, AC 1 min	2500	V
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note10)	175	$^{\circ}\text{C}$
T_{Cmax}	Maximum case temperature	(Note4, 10)	125	
T_{vjop}	Operating junction temperature	Continuous operation (under switching) (Note10)	-40 ~ +150	$^{\circ}\text{C}$
T_{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS ($T_{vj}=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
V _{GE(th)}	Gate-emitter threshold voltage	I _C =10 mA, V _{CE} =10 V		5.4	6.0	6.6	V
V _{CESat} (Terminal)	Collector-emitter saturation voltage	I _C =100 A, V _{GE} =15 V, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	1.60	1.95	V
			T _{vj} =125 °C	-	1.80	-	
			T _{vj} =150 °C	-	1.85	-	
V _{CESat} (Chip)		I _C =100 A, V _{GE} =15 V, (Note5)	T _{vj} =25 °C	-	1.55	1.80	V
			T _{vj} =125 °C	-	1.75	-	
			T _{vj} =150 °C	-	1.80	-	
C _{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	22.8	nF
C _{oes}	Output capacitance			-	-	0.8	
C _{res}	Reverse transfer capacitance			-	-	0.3	
Q _G	Gate charge	V _{CC} =600 V, I _C =100 A, V _{GE} =15 V		-	0.75	-	μC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =100 A, V _{GE} =±15 V, R _G =3.9 Ω, Inductive load		-	-	400	ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	500	
t _f	Fall time			-	-	500	

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ELECTRICAL CHARACTERISTICS (cont.; $T_{vj}=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions		Limits			Unit	
				Min.	Typ.	Max.		
V_{EC} (Note1) (Terminal)	Emitter-collector voltage	$I_E=100\text{ A}$, G-E short-circuited, Refer to the figure of test circuit (Note5)	$T_{vj}=25\text{ }^{\circ}\text{C}$	-	1.50	1.95	V	
V_{EC} (Note1) (Chip)			$T_{vj}=125\text{ }^{\circ}\text{C}$	-	1.60	-		V
			$T_{vj}=150\text{ }^{\circ}\text{C}$	-	1.65	-		
	$I_E=100\text{ A}$, G-E short-circuited, (Note5)	$T_{vj}=25\text{ }^{\circ}\text{C}$	-	1.45	1.75	V		
	$T_{vj}=125\text{ }^{\circ}\text{C}$	-	1.45	-	V			
	$T_{vj}=150\text{ }^{\circ}\text{C}$	-	1.45	-			V	
t_{rr} (Note1)	Reverse recovery time	$V_{CC}=600\text{ V}$, $I_E=100\text{ A}$, $V_{GE}=\pm 15\text{ V}$, $R_G=3.9\text{ }\Omega$, Inductive load	-	-		300		ns
Q_{rr} (Note1)	Reverse recovery charge		-	12	-	μC		
E_{on}	Turn-on switching energy per pulse	$V_{CC}=600\text{ V}$, $I_C=I_E=100\text{ A}$, $V_{GE}=\pm 15\text{ V}$, $R_G=3.9\text{ }\Omega$, $T_{vj}=150\text{ }^{\circ}\text{C}$, Inductive load	-	9.2	-	mJ		
E_{off}	Turn-off switching energy per pulse		-	10.4	-		mJ	
E_{rr} (Note1)	Reverse recovery energy per pulse		-	8.2	-	mJ		
$R_{CC'+EE'}$	Internal lead resistance		Main terminals-chip, per switch, $T_C=25\text{ }^{\circ}\text{C}$ (Note4)	-	2.0	-	m Ω	
r_g	Internal gate resistance	Per switch	-	0	-	Ω		

BRAKE PART IGBT/DIODE

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μA
V _{GE(th)}	Gate-emitter threshold voltage	I _C =7.5 mA, V _{CE} =10 V		5.4	6.0	6.6	V
V _{CESat} (Terminal)	Collector-emitter saturation voltage	I _C =75 A, V _{GE} =15 V, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	1.65	2.00	V
			T _{vj} =125 °C	-	1.80	-	
			T _{vj} =150 °C	-	1.85	-	
V _{CESat} (Chip)		I _C =75 A, V _{GE} =15 V, (Note5)	T _{vj} =25 °C	-	1.60	1.85	V
			T _{vj} =125 °C	-	1.75	-	
			T _{vj} =150 °C	-	1.80	-	
C _{ies}	Input capacitance	V _{CE} =10 V, G-E short-circuited		-	-	18.2	nF
C _{oes}	Output capacitance			-	-	0.5	
C _{res}	Reverse transfer capacitance			-	-	0.2	
Q _G	Gate charge	V _{CC} =600 V, I _C =75 A, V _{GE} =15 V		-	0.57	-	μC
t _{d(on)}	Turn-on delay time	V _{CC} =600 V, I _C =75 A, V _{GE} =±15 V,		-	-	400	ns
t _r	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time	R _G =5.6 Ω, Inductive load		-	-	500	
t _f	Fall time			-	-	500	
E _{on}	Turn-on switching energy per pulse	V _{CC} =600 V, I _C =75 A, V _{GE} =±15 V,		-	9.3	-	mJ
E _{off}	Turn-off switching energy per pulse	R _G =5.6 Ω, T _{vj} =150 °C, Inductive load		-	7.8	-	
r _g	Internal gate resistance	-		-	4.0	-	Ω
I _{RRM}	Reverse current	V _R =V _{RRM} , G-E short-circuited		-	-	1.0	mA
V _F (Terminal)	Forward voltage	I _F =75 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T _{vj} =25 °C	-	1.65	2.10	V
			T _{vj} =125 °C	-	1.80	-	
			T _{vj} =150 °C	-	1.85	-	
V _F (Chip)		I _F =75 A, G-E short-circuited, (Note5)	T _{vj} =25 °C	-	1.50	1.90	V
			T _{vj} =125 °C	-	1.50	-	
			T _{vj} =150 °C	-	1.50	-	
t _{rr}	Reverse recovery time	V _{CC} =600 V, I _F =75 A, V _{GE} =±15 V,		-	-	300	ns
Q _{rr}	Reverse recovery charge	R _G =5.6 Ω, Inductive load		-	8.0	-	μC
E _{rr}	Reverse recovery energy per pulse	V _{CC} =600 V, I _F =75 A, V _{GE} =±15 V, R _G =5.6 Ω, T _{vi} =150 °C, Inductive load		-	5.2	-	mJ

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ELECTRICAL CHARACTERISTICS (cont.; $T_{vj}=25\text{ }^{\circ}\text{C}$, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
R_{25}	Zero-power resistance	$T_C=25\text{ }^{\circ}\text{C}$ (Note4)	4.85	5.00	5.15	k Ω
$\Delta R/R$	Deviation of resistance	$R_{100}=493\text{ }\Omega$, $T_C=100\text{ }^{\circ}\text{C}$ (Note4)	-7.3	-	+7.8	%
$B_{(25/50)}$	B-constant	Approximate by equation (Note6)	-	3375	-	K
P_{25}	Power dissipation	$T_C=25\text{ }^{\circ}\text{C}$ (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	264	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	391	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, Brake IGBT (Note4)	-	-	339	K/kW
$R_{th(j-c)D}$		Junction to case, Brake DIODE (Note4)	-	-	480	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module,	Thermal grease applied (Note4, 7, 10)		-	K/kW
			PC-TIM applied (Note4, 8, 10)		11.5	

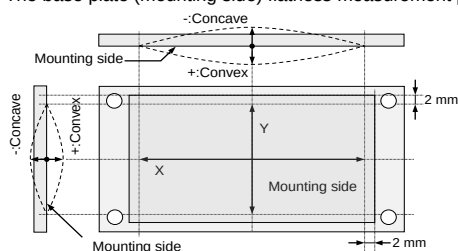
MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_t	Mounting torque	Main terminals M 6 screw	3.5	4.0	4.5	N·m
M_s	Mounting torque	Mounting to heat sink M 5 screw	2.5	3.0	3.5	N·m
d_s	Creepage distance	Terminal to terminal	17	-	-	mm
		Terminal to base plate	18.4	-	-	
d_a	Clearance	Terminal to terminal	10	-	-	mm
		Terminal to base plate	16.2	-	-	
e_c	Flatness of base plate	On the centerline X, Y (Note9)	± 0	-	+200	μm
m	mass	-	-	330	-	g

*. This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

- Junction temperature (T_{vj}) should not increase beyond $T_{vj\text{max}}$ rating.
- Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed $T_{vj\text{max}}$ rating.
- Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips.
Refer to the figure of chip location.
- Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
- $B_{(25/50)} = \ln\left(\frac{R_{25}}{R_{50}}\right) / \left(\frac{1}{T_{25}} - \frac{1}{T_{50}}\right)$
 R_{25} : resistance at absolute temperature T_{25} [K]; $T_{25}=25\text{ }^{\circ}\text{C}+273.15=298.15\text{ [K]}$
 R_{50} : resistance at absolute temperature T_{50} [K]; $T_{50}=50\text{ }^{\circ}\text{C}+273.15=323.15\text{ [K]}$
- Typical value is measured by using thermally conductive grease of $\lambda=0.9\text{ W/(m}\cdot\text{K)}/D_{(C-S)}=50\text{ }\mu\text{m}$.
- Typical value is measured by using PC-TIM of $\lambda=3.4\text{ W/(m}\cdot\text{K)}/D_{(C-S)}=50\text{ }\mu\text{m}$.
- The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



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10. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition ($T_{vj\ max}$, $T_{vj\ op}$, $T_{C\ max}$) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.
11. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness : t1.6.

Type	Size	Tightening torque	Recommended tightening method
(1) PT□	K25×8	$0.55 \pm 0.055\ N \cdot m$	by handwork (equivalent to 30 rpm) by mechanical screw driver) ~ 600 rpm (by mechanical screw driver)
(2) PT□	K25×10	$0.75 \pm 0.075\ N \cdot m$	
(3) DELTA PT□	25×8	$0.55 \pm 0.055\ N \cdot m$	
(4) DELTA PT□	25×10	$0.75 \pm 0.075\ N \cdot m$	
(5) B1 tapping screw	φ2.6×10 or φ2.6×12	$0.75 \pm 0.075\ N \cdot m$	

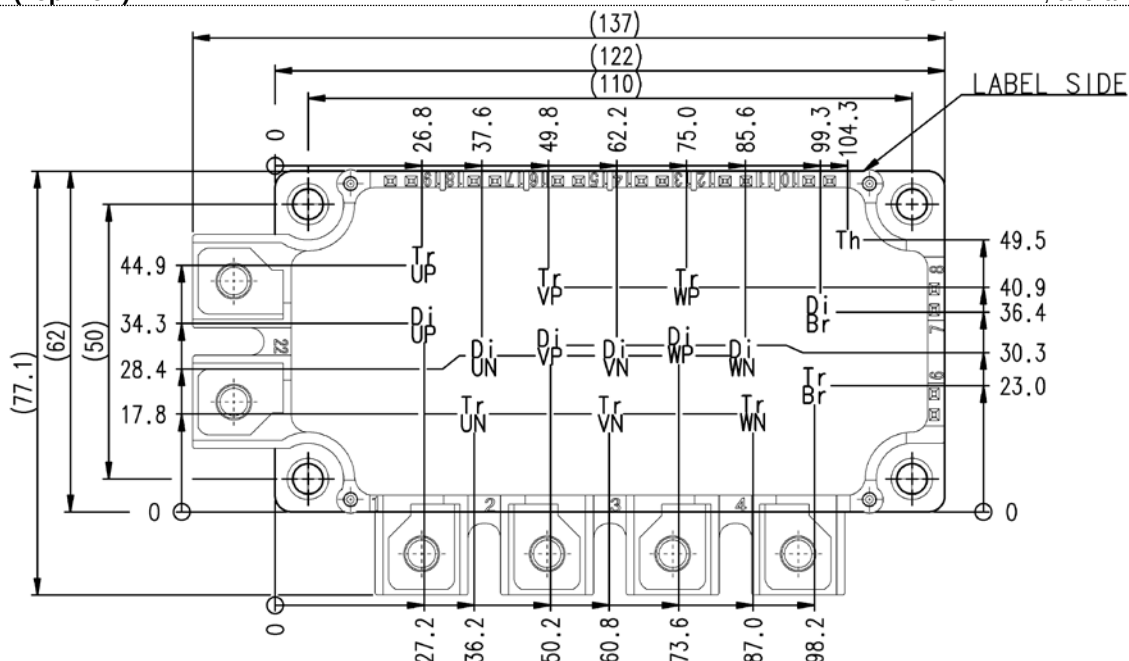
RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across P-N terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G*P-E*P/G*N-E*N/GB-EB terminals (*=U,V,W)	13.5	15.0	16.5	V
R_G	External gate resistance	Inverter IGBT, Per switch	3.9	-	39	Ω
		Brake IGBT	5.6	-	56	

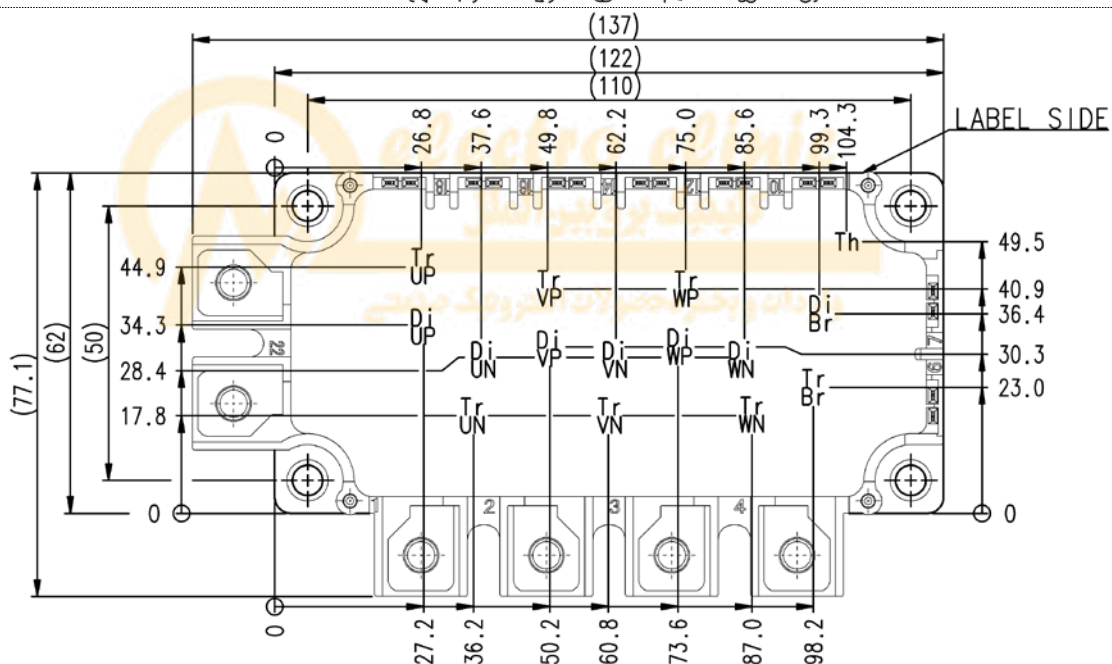


CHIP LOCATION (Top view)

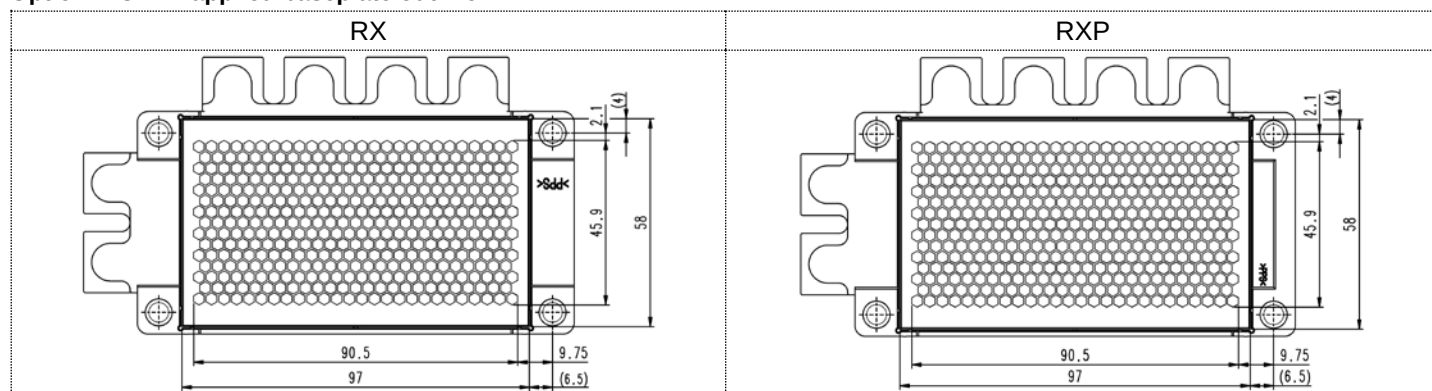
RX

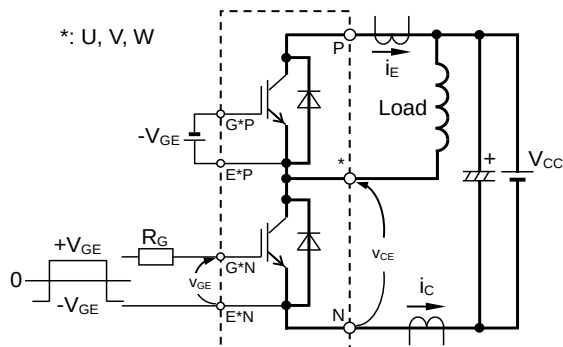


RXP

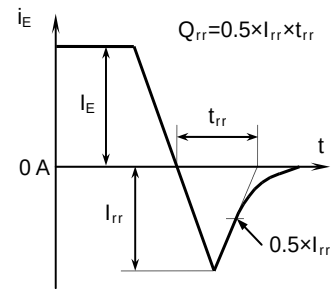
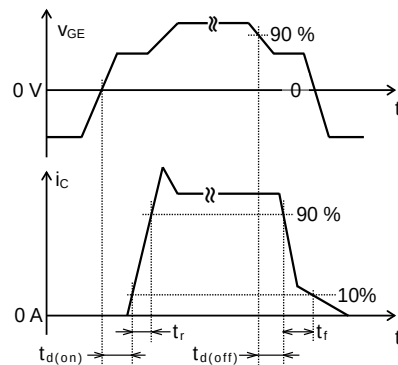
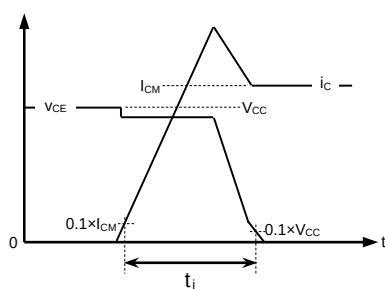


Option: PC-TIM applied baseplate outline

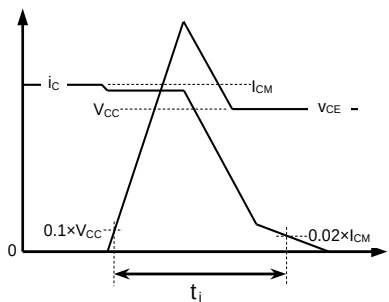


TEST CIRCUIT AND WAVEFORMS

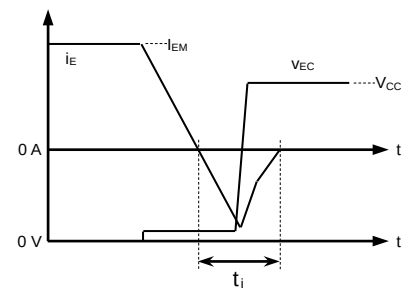
Switching characteristics test circuit and waveforms

 t_{rr} , Q_{rr} characteristics test waveform

IGBT Turn-on switching energy



IGBT Turn-off switching energy



FWD Reverse recovery energy

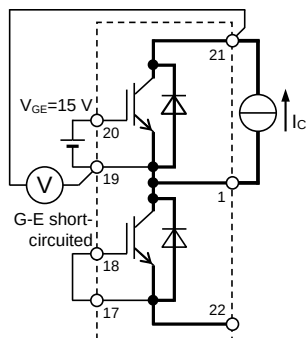
Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

CM100RX-24T/CM100RXP-24T

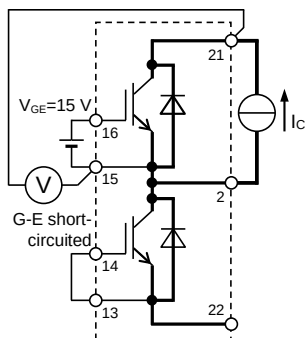
HIGH POWER SWITCHING USE

INSULATED TYPE

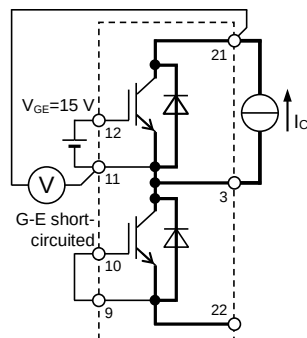
TEST CIRCUIT



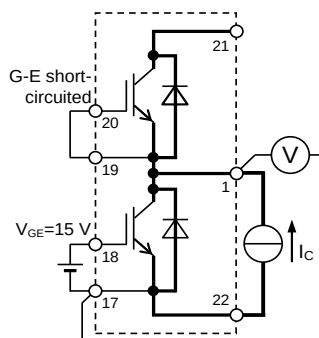
TrUP



TrVP

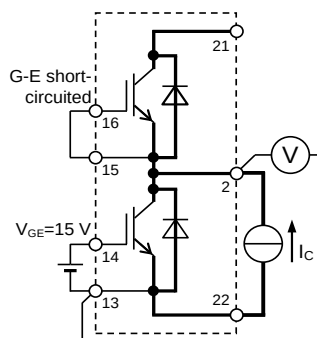


TrWP



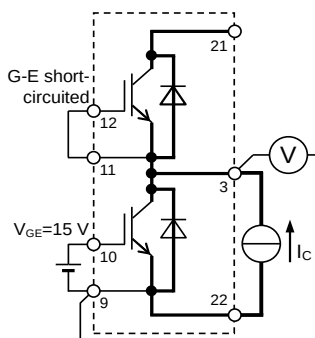
TrUN

Gate-emitter GVP-EVP, GVN-EVN,
short-circuited GWP-EWP, GWN-EWN
GB-EB



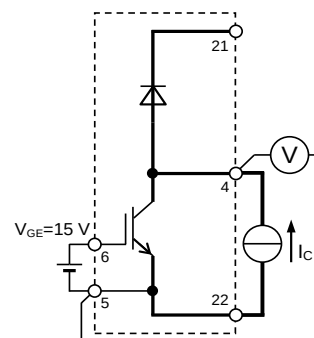
TrVN

Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GWP-EWP, GWN-EWN
GB-EB

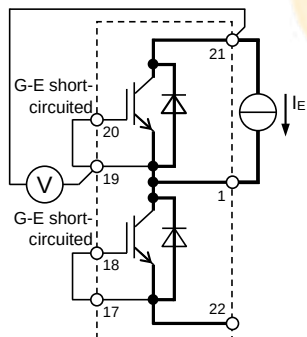


TrWN

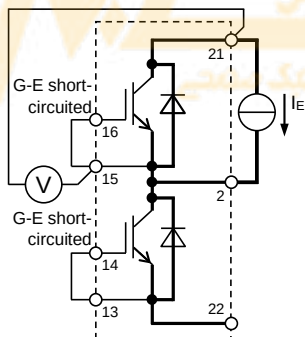
Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GVP-EVP, GVN-EVN
GB-EB



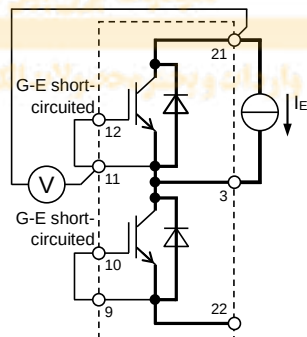
Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GVP-EVP, GVN-EVN,
GWP-EWP, GWN-EWN

 V_{CEsat} characteristics test circuit

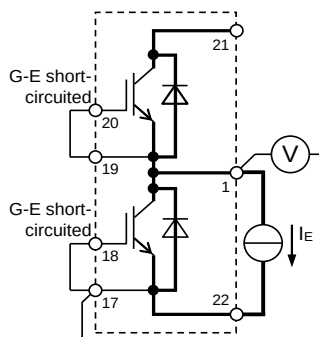
DiUP



DiVP

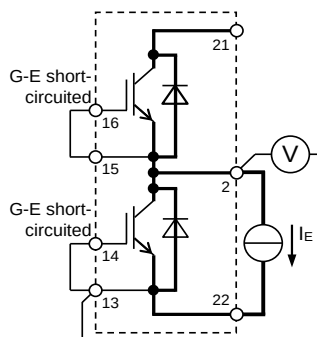


DiWP



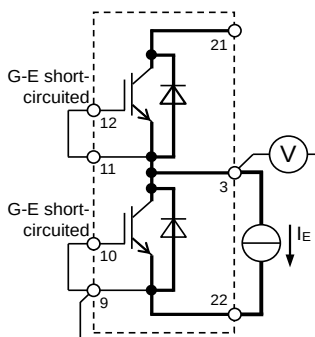
DiUN

Gate-emitter GVP-EVP, GVN-EVN,
short-circuited GWP-EWP, GWN-EWN
GB-EB



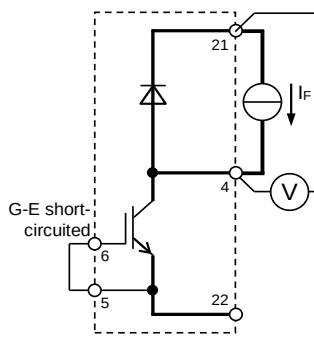
DiVN

Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GWP-EWP, GWN-EWN
GB-EB



DiWN

Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GVP-EVP, GVN-EVN
GB-EB



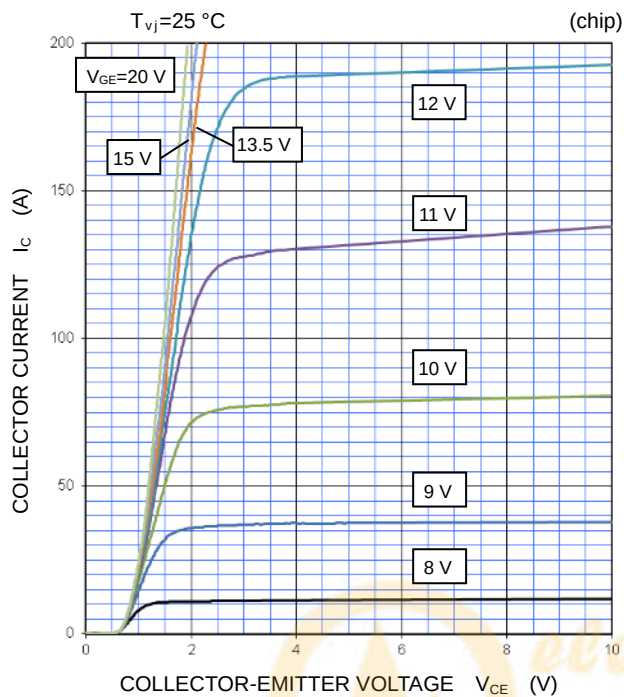
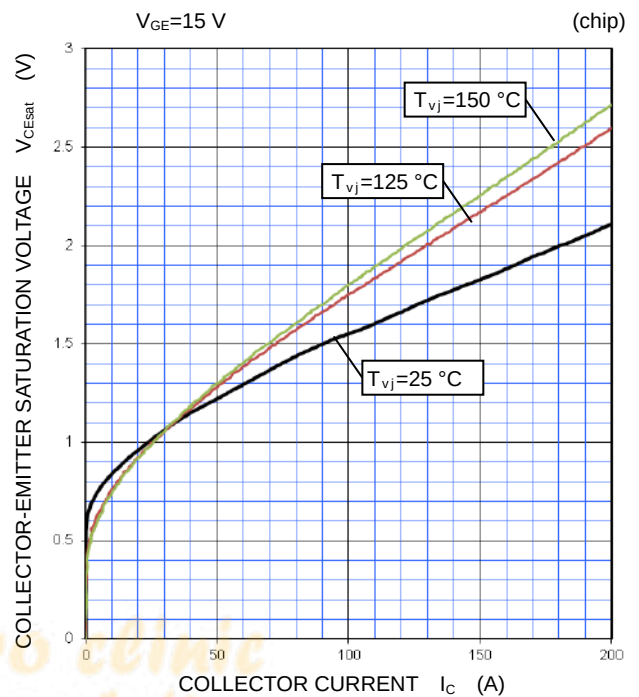
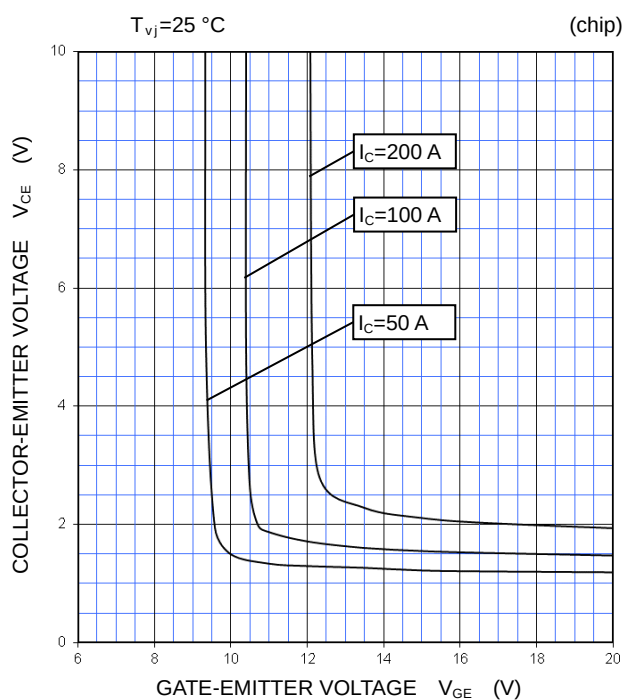
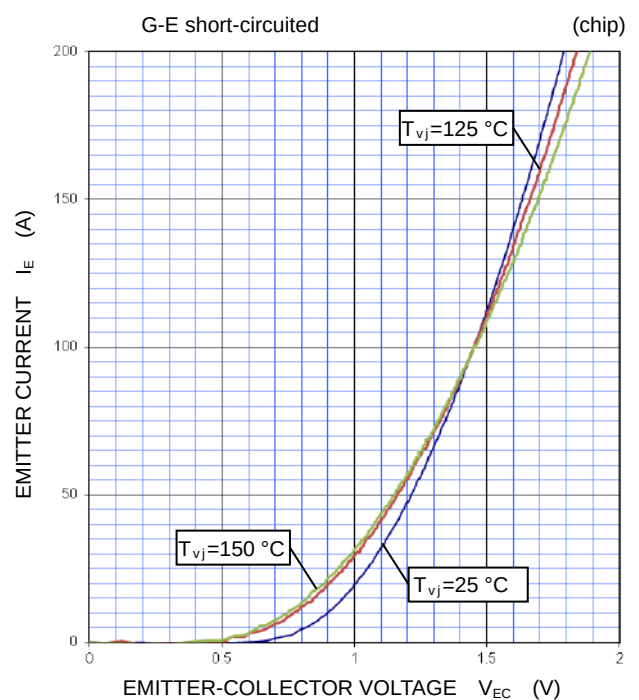
Gate-emitter GUP-EUP, GUN-EUN,
short-circuited GVP-EVP, GVN-EVN,
GWP-EWP, GWN-EWN

 V_{EC} characteristics test circuit V_F characteristics test circuit

CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE

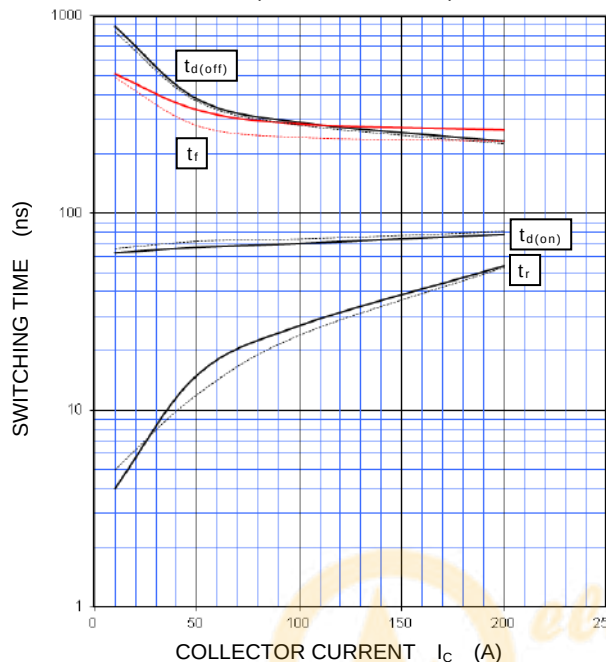
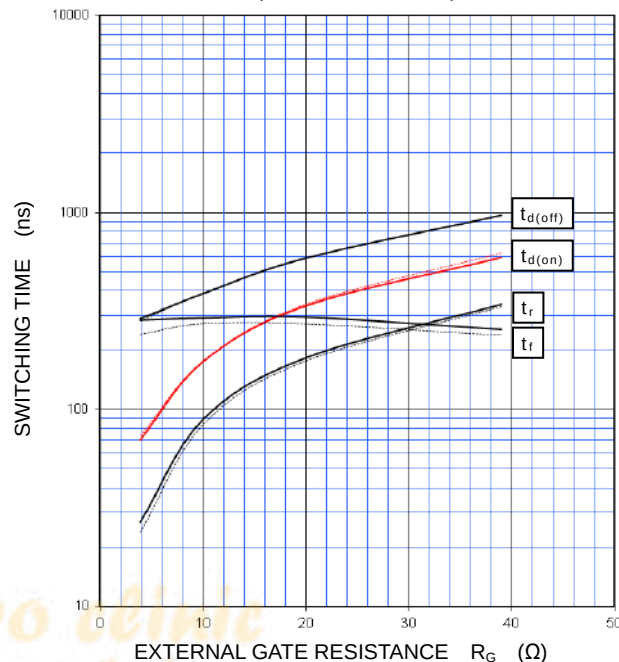
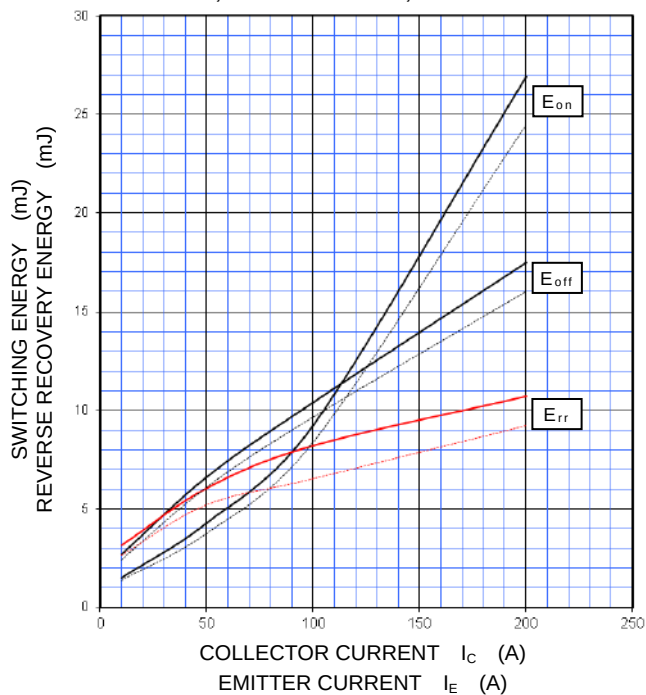
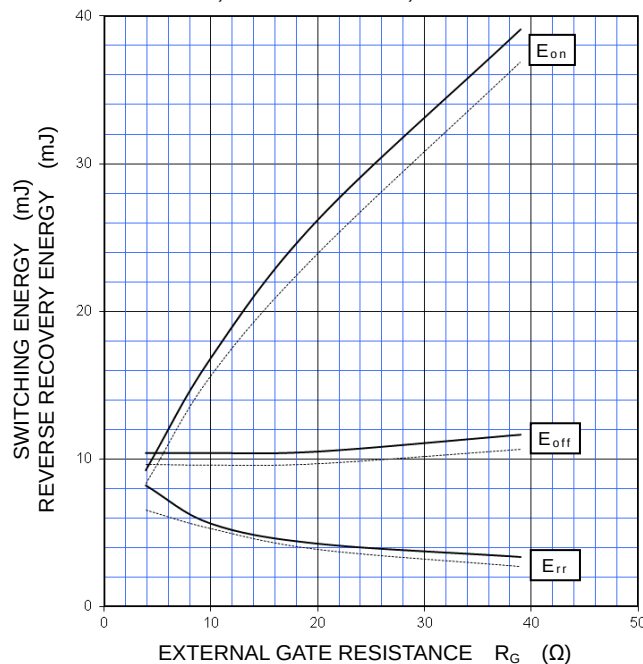
INSULATED TYPE

PERFORMANCE CURVES**INVERTER PART****OUTPUT CHARACTERISTICS
(TYPICAL)****COLLECTOR-EMITTER SATURATION VOLTAGE
CHARACTERISTICS
(TYPICAL)****COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS
(TYPICAL)****FREE WHEELING DIODE
FORWARD CHARACTERISTICS
(TYPICAL)**

CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE

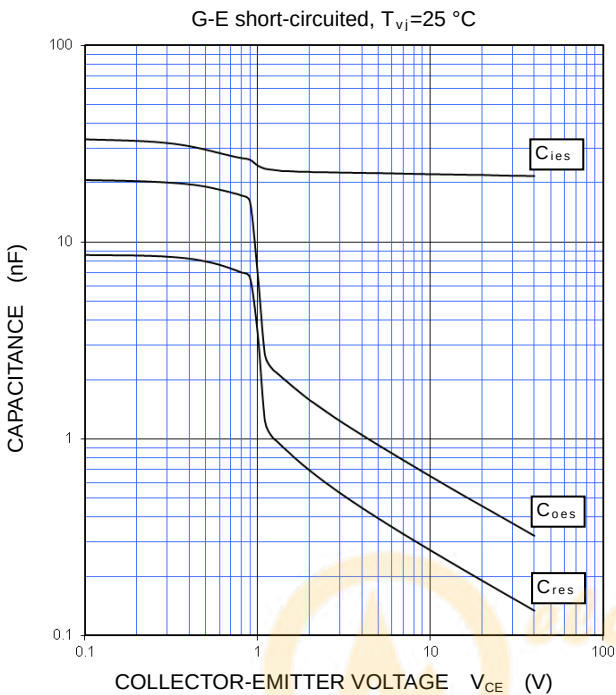
INSULATED TYPE

PERFORMANCE CURVES**INVERTER PART****HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $R_G=3.9\ \Omega$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$ **HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $I_C=100\text{ A}$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$ **HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $R_G=3.9\ \Omega$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD,
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$, PER PULSE**HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $I_C/I_E=100\text{ A}$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD,
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$, PER PULSE

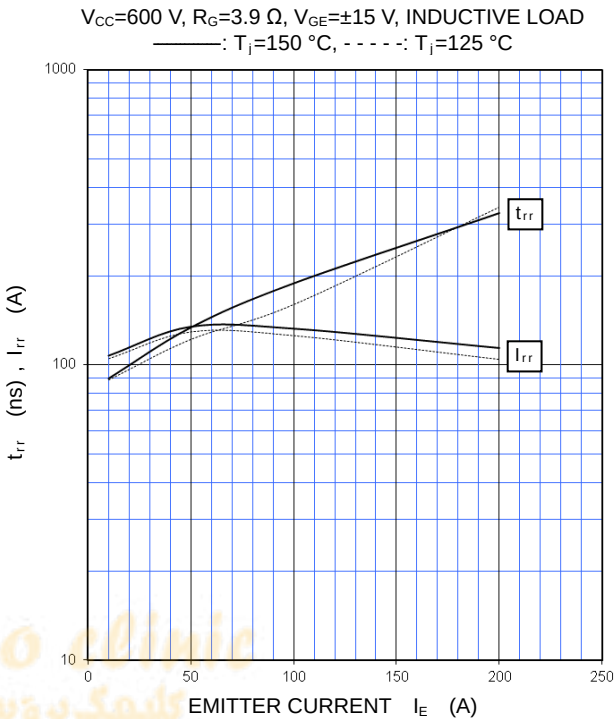
PERFORMANCE CURVES

INVERTER PART

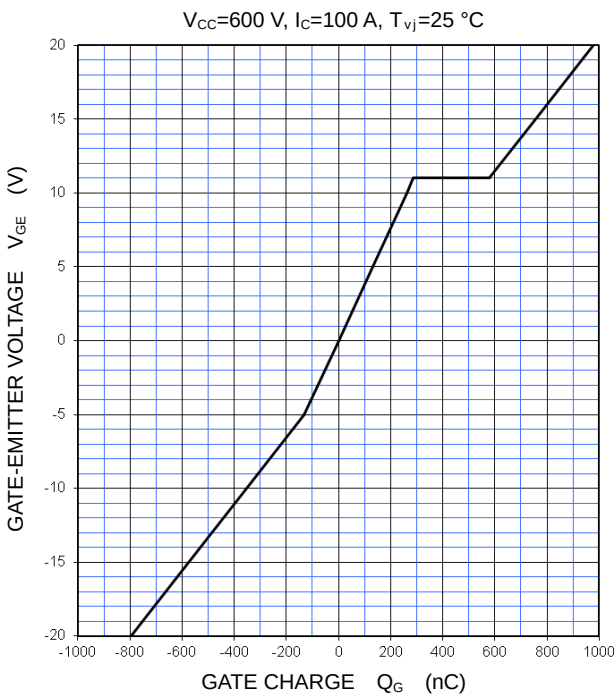
CAPACITANCE CHARACTERISTICS
(TYPICAL)



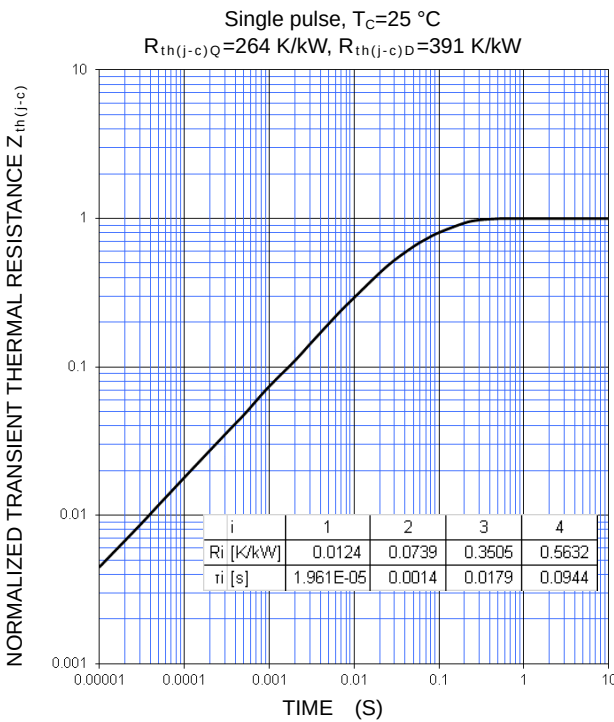
FREE WHEELING DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)



GATE CHARGE CHARACTERISTICS
(TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)



CM100RX-24T/CM100RXP-24T

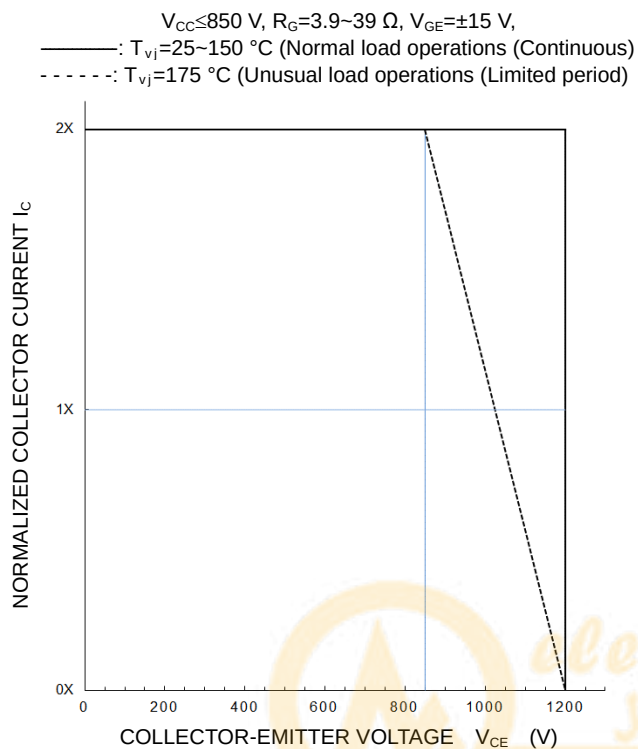
HIGH POWER SWITCHING USE

INSULATED TYPE

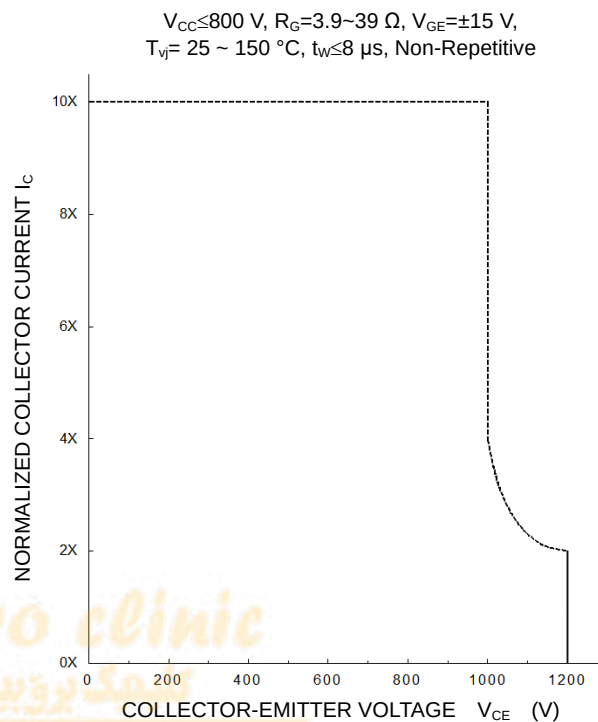
PERFORMANCE CURVES

INVERTER PART

TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)



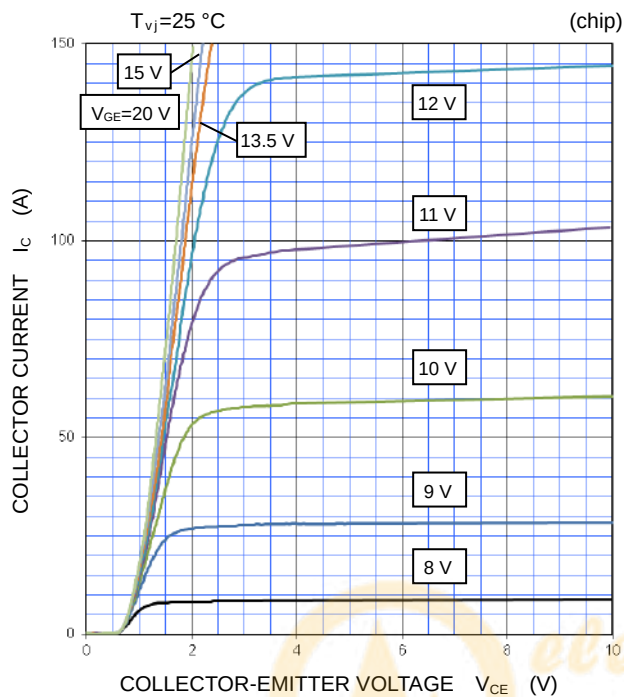
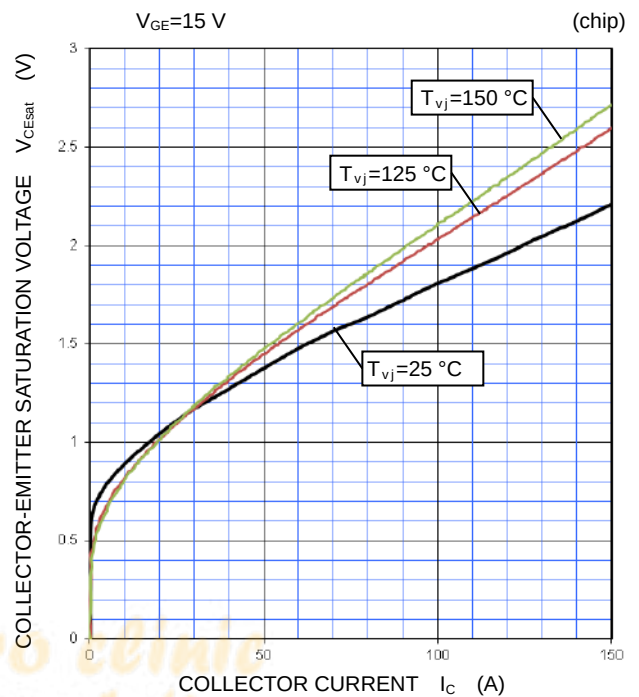
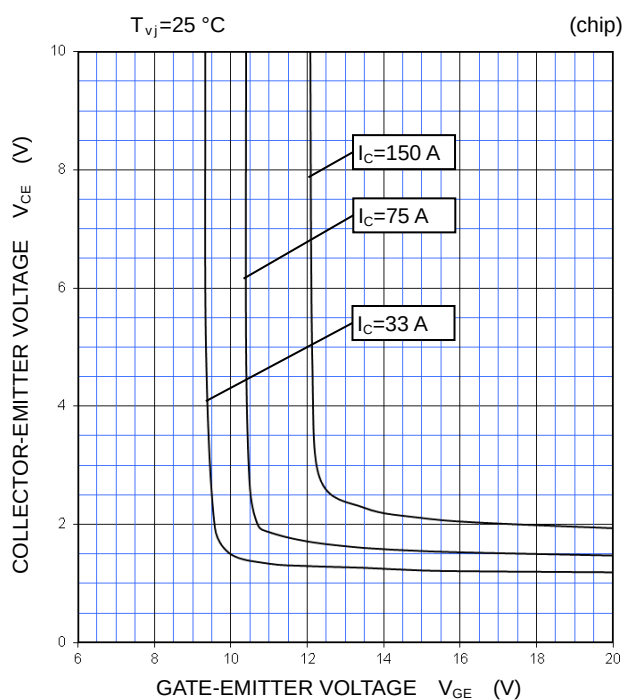
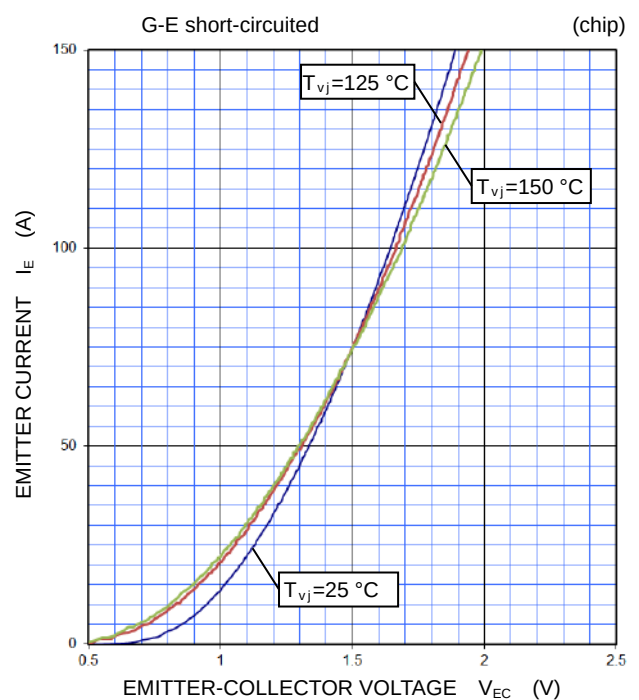
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)



CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE

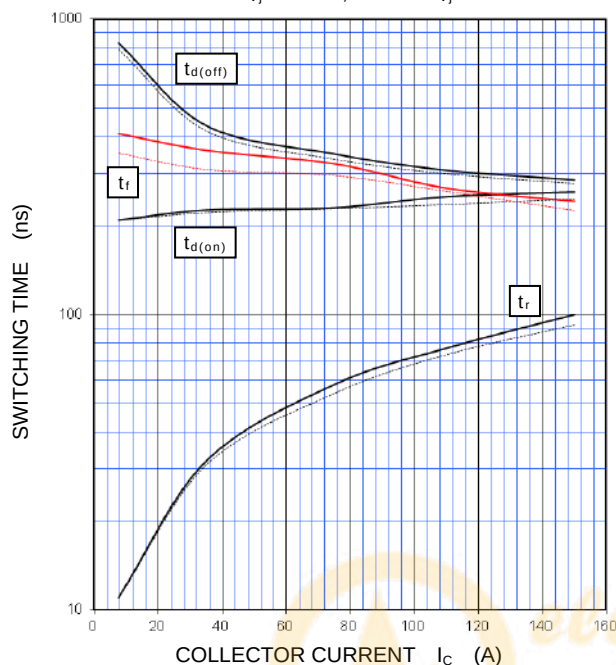
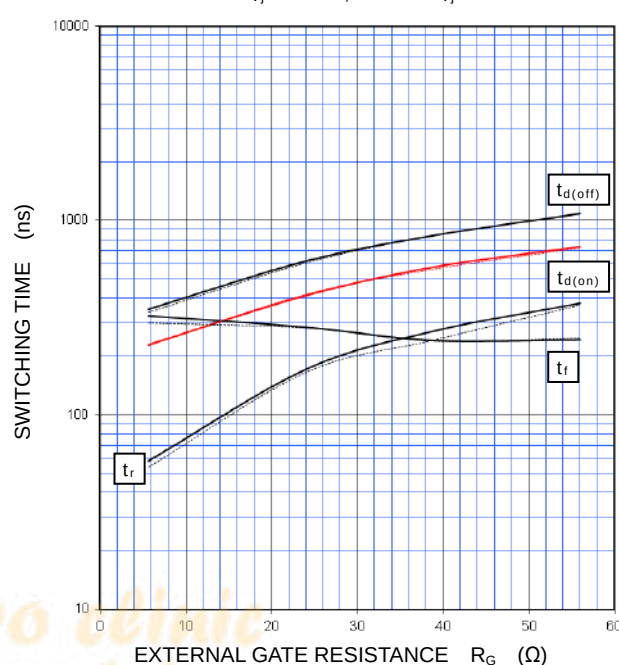
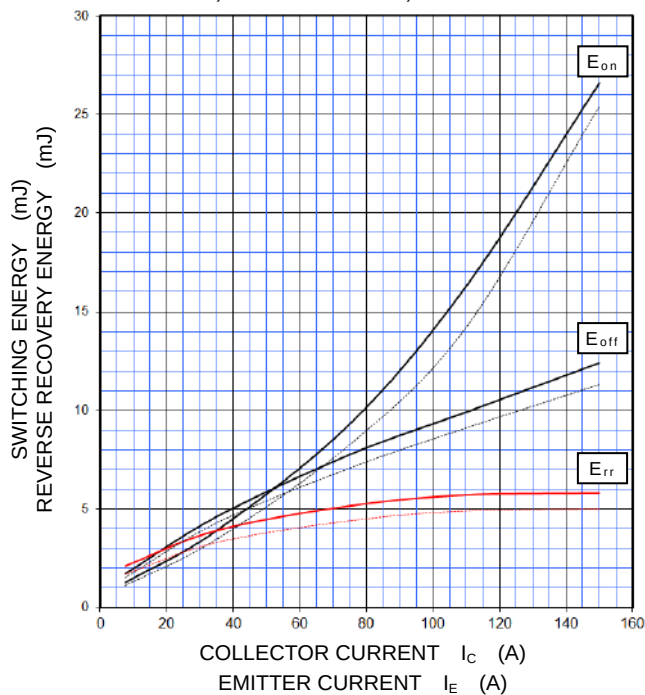
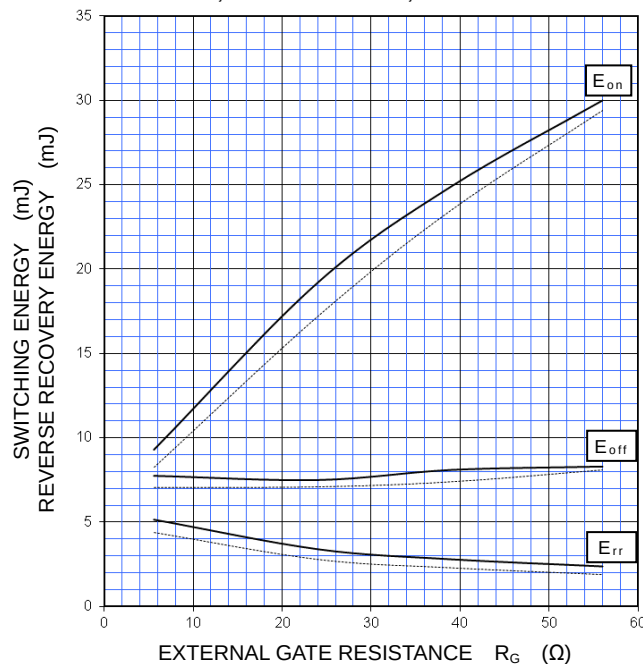
INSULATED TYPE

PERFORMANCE CURVES**BRAKE PART****OUTPUT CHARACTERISTICS
(TYPICAL)****COLLECTOR-EMITTER SATURATION VOLTAGE
CHARACTERISTICS
(TYPICAL)****COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS
(TYPICAL)****DIODE
FORWARD CHARACTERISTICS
(TYPICAL)**

CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE

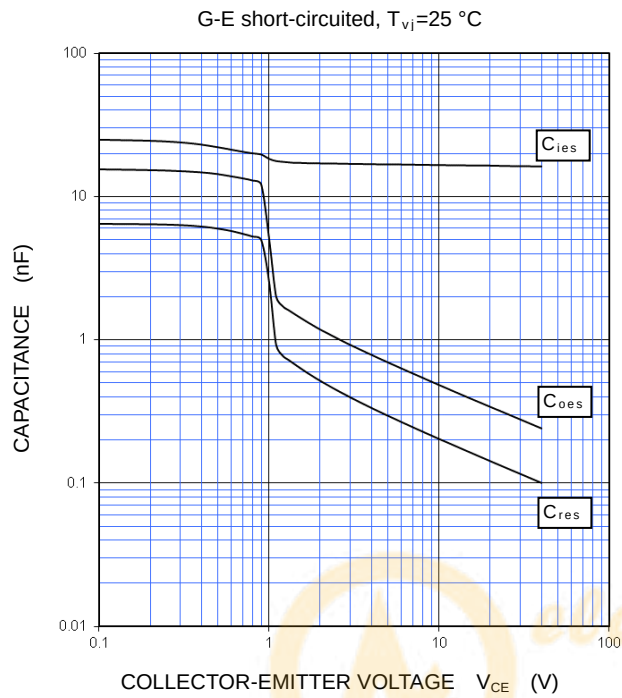
INSULATED TYPE

PERFORMANCE CURVES**BRAKE PART****HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $R_G=5.6\ \Omega$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$ **HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $I_C=75\text{ A}$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$ **HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $R_G=5.6\ \Omega$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD,
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$, PER PULSE**HALF-BRIDGE SWITCHING CHARACTERISTICS
(TYPICAL)** $V_{CC}=600\text{ V}$, $I_C/I_E=75\text{ A}$, $V_{GE}=\pm 15\text{ V}$, INDUCTIVE LOAD,
—: $T_{vj}=150\text{ }^\circ\text{C}$, - - - -: $T_{vj}=125\text{ }^\circ\text{C}$, PER PULSE

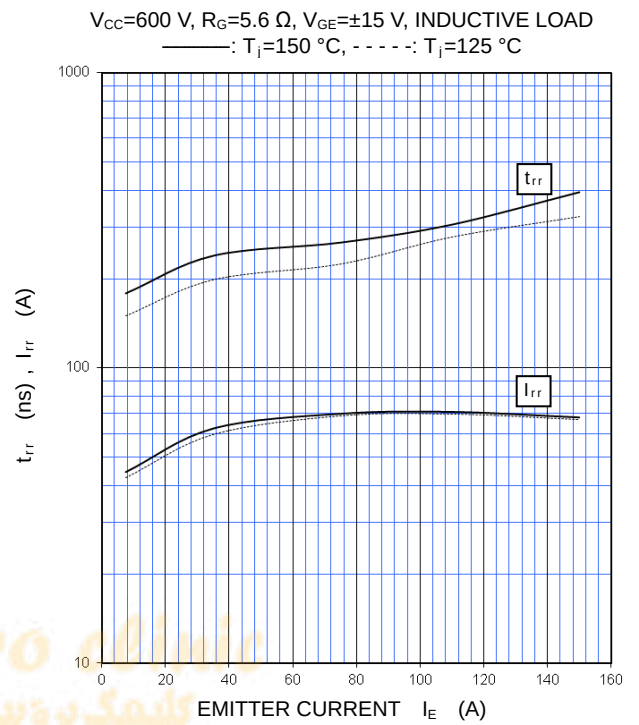
PERFORMANCE CURVES

BRAKE PART

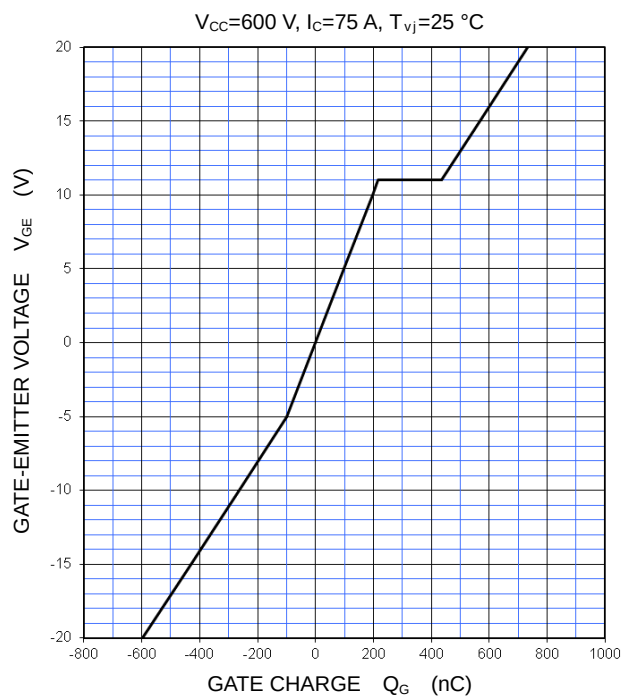
CAPACITANCE CHARACTERISTICS
(TYPICAL)



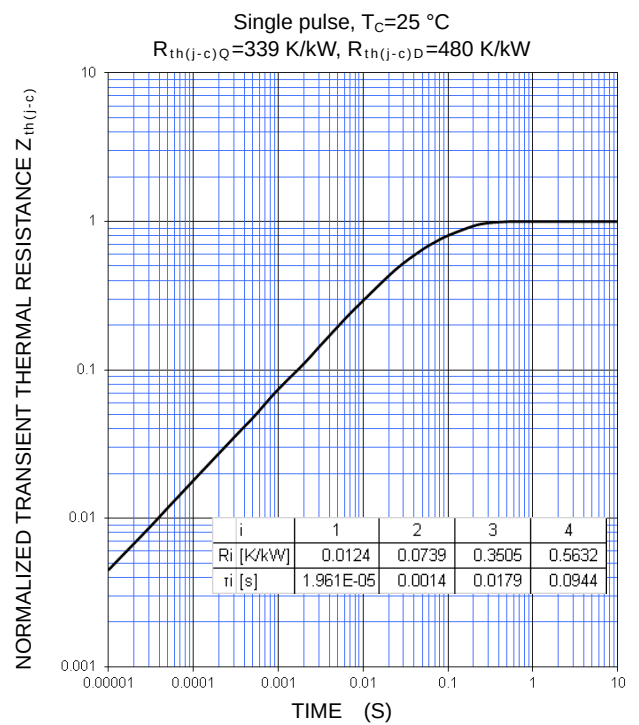
DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)



GATE CHARGE CHARACTERISTICS
(TYPICAL)



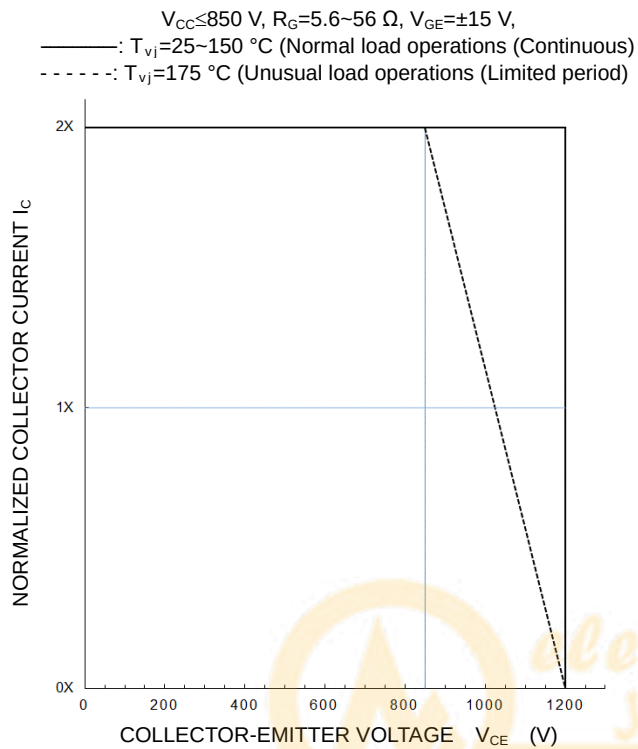
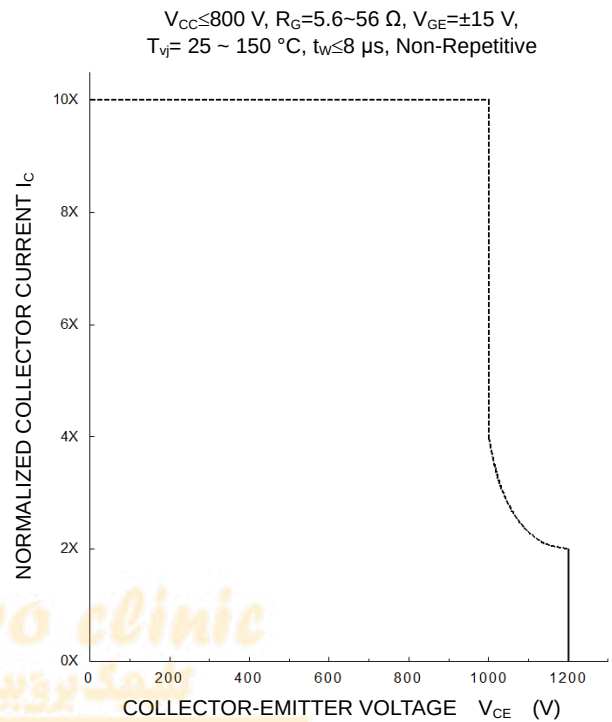
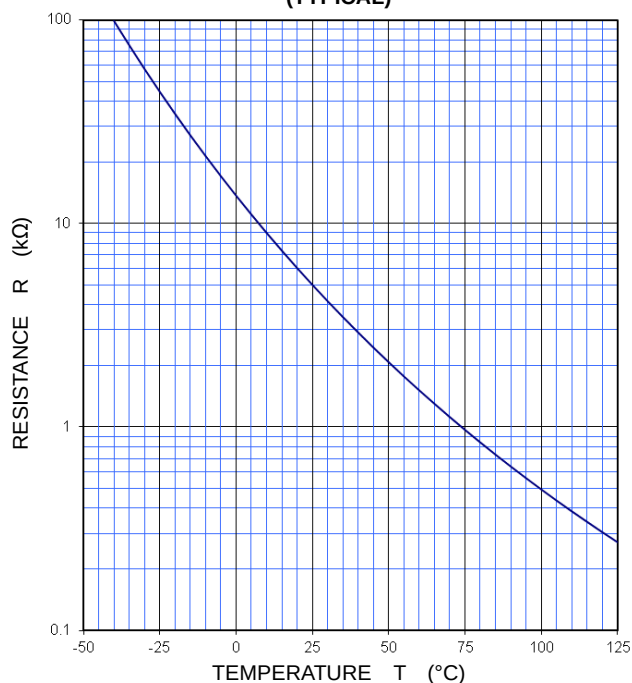
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)



CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE

PERFORMANCE CURVES**BRAKE PART****TURN-OFF SWITCHING SAFE OPERATING AREA
(REVERSE BIAS SAFE OPERATING AREA)
(MAXIMUM)****SHORT-CIRCUIT SAFE OPERATING AREA
(MAXIMUM)****NTC thermistor part****TEMPERATURE CHARACTERISTICS
(TYPICAL)**

Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

CM100RX-24T/CM100RXP-24T

HIGH POWER SWITCHING USE

INSULATED TYPE

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