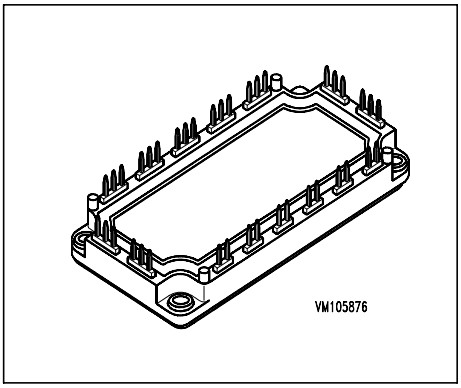
IGBT Power Module

Preliminary data

- Solderable Power module
- 3-phase full-bridge
- Including fast free-wheel diodes
- Package with insulated metal base plate



Type	V_{CE}	I_C	Package	Ordering Code
BSM 75 GD 120 DN2	1200V	103A	ECONOPACK 3	C67070-A2516-A67

Maximum Ratings

Parameter	Symbol	Values	Unit
Collector-emitter voltage	V_{CE}	1200	V
Collector-gate voltage R_{GE} = 20 kΩ	V_{CGR}	1200	
Gate-emitter voltage	V_{GE}	± 20	
DC collector current T_C = 25 °C T_C = 80 °C	I_C	103 75	A
Pulsed collector current, t_p = 1 ms T_C = 25 °C T_C = 80 °C	I_{Cpuls}	206 150	
Power dissipation per IGBT T_C = 25 °C	P_{tot}	520	W
Chip temperature	T_j	+ 150	°C
Storage temperature	T_{stg}	-55 ... + 150	
Thermal resistance, chip case	R_{thJC}	≤ 0.235	K/W
Diode thermal resistance, chip case	R_{thJCD}	≤ 0.55	
Insulation test voltage, t = 1min.	V_{is}	2500	Vac
Creepage distance	-	16	mm
Clearance	-	11	
DIN humidity category, DIN 40 040	-	F	-
IEC climatic category, DIN IEC 68-1	-	55 / 150 / 56	

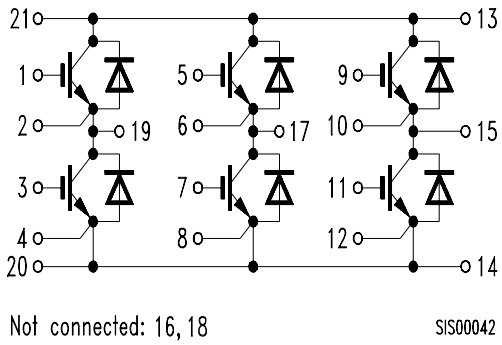
Electrical Characteristics, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Gate threshold voltage $V_{GE} = V_{CE}$, I_C = 2 mA	$V_{GE(th)}$				V
Collector-emitter saturation voltage V_{GE} = 15 V, I_C = 75 A, T_j = 25 °C V_{GE} = 15 V, I_C = 75 A, T_j = 125 °C	$V_{CE(sat)}$	 - -	 2.5 3.1	 3 3.7	
Zero gate voltage collector current V_{CE} = 1200 V, V_{GE} = 0 V, T_j = 25 °C V_{CE} = 1200 V, V_{GE} = 0 V, T_j = 125 °C	I_{CES}	 - -	 1 4	 1.5 -	mA
Gate-emitter leakage current V_{GE} = 20 V, V_{CE} = 0 V	I_{GES}	-	-	320	nA
AC Characteristics					
Transconductance V_{CE} = 20 V, I_C = 75 A	g_{fs}	31	-	-	S
Input capacitance V_{CE} = 25 V, V_{GE} = 0 V, f = 1 MHz	C_{iss}	-	5.1	-	nF
Output capacitance V_{CE} = 25 V, V_{GE} = 0 V, f = 1 MHz	C_{oss}	-	0.72	-	
Reverse transfer capacitance V_{CE} = 25 V, V_{GE} = 0 V, f = 1 MHz	C_{rss}	-	0.38	-	

Electrical Characteristics, at T_j = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Switching Characteristics, Inductive Load at T_j = 125 °C					
Turn-on delay time V_{CC} = 600 V, V_{GE} = 15 V, I_C = 75 A R_{Gon} = 15 Ω	$t_{d(on)}$	- -	 30	 60	ns
Rise time V_{CC} = 600 V, V_{GE} = 15 V, I_C = 75 A R_{Gon} = 15 Ω	t_r	- -	 70	 140	
Turn-off delay time V_{CC} = 600 V, V_{GE} = -15 V, I_C = 75 A R_{Goff} = 15 Ω	$t_{d(off)}$	- -	 450	 600	
Fall time V_{CC} = 600 V, V_{GE} = -15 V, I_C = 75 A R_{Goff} = 15 Ω	t_f	- -	 70	 100	
Free-Wheel Diode					
Diode forward voltage I_F = 75 A, V_{GE} = 0 V, T_j = 25 °C I_F = 75 A, V_{GE} = 0 V, T_j = 125 °C	V_F	 - -	 2.3 1.8	 2.8 -	V
Reverse recovery time I_F = 75 A, V_R = -600 V, V_{GE} = 0 V di_F/dt = -900 A/µs, T_j = 125 °C	t_{rr}	 -	 0.125	 -	µs
Reverse recovery charge I_F = 75 A, V_R = -600 V, V_{GE} = 0 V di_F/dt = -800 A/µs, T_j = 25 °C di_F/dt = -800 A/µs, T_j = 125 °C di_F/dt = -900 A/µs, T_j = 25 °C	Q_{rr}	 - -	 3.2 10	 - -	µC

Circuit Diagram



Package Outlines

Dimensions in mm

Weight: 300 g